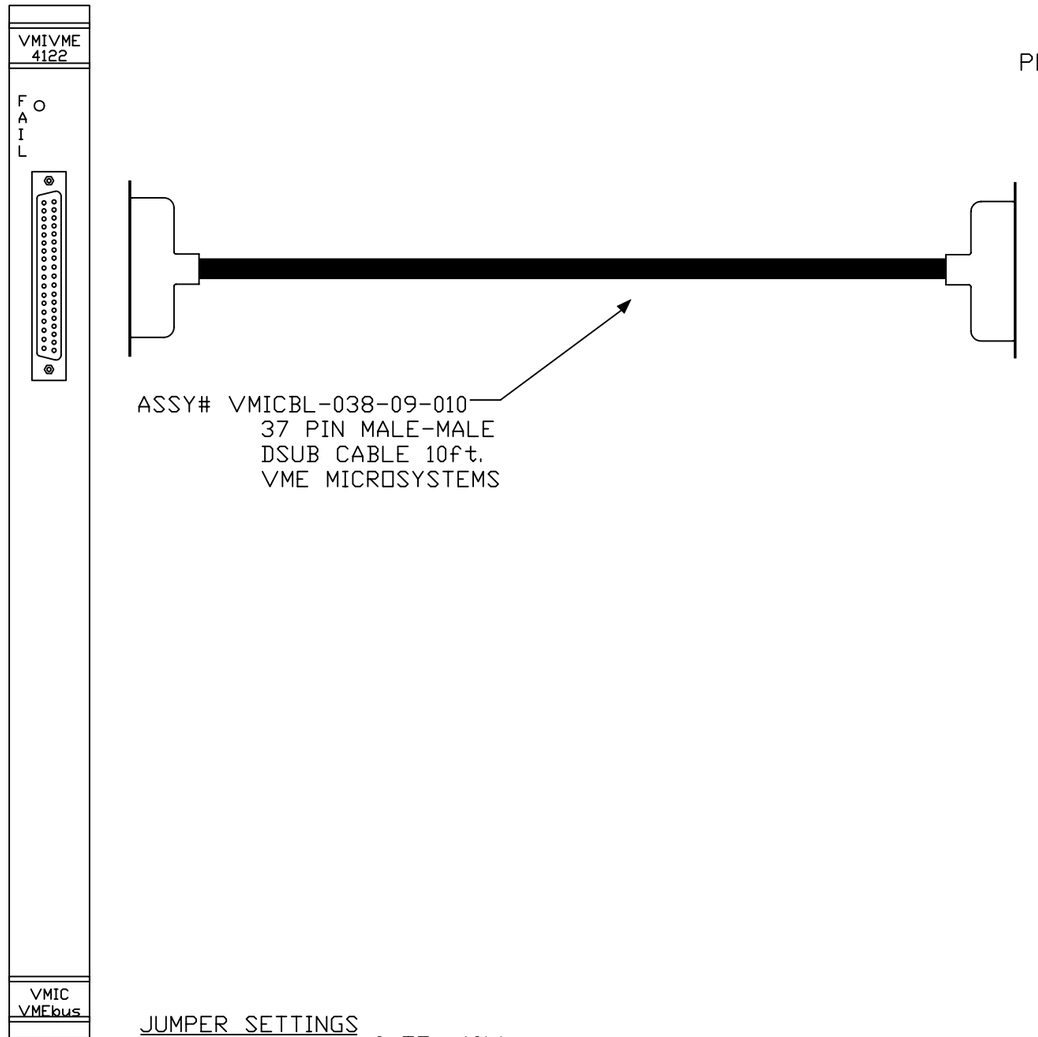
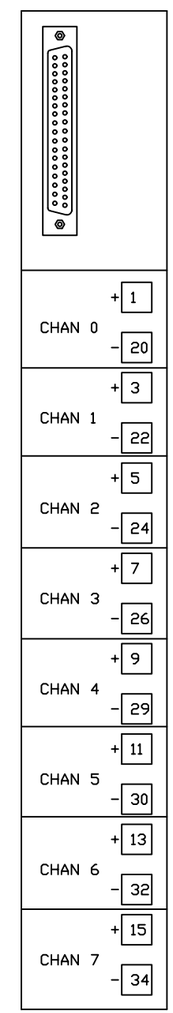
VMIVME 4122 (8 CHAN., 12 BIT ANALOG OUTPUT MODULE

Phil Pape, Controls Division 5/2/2002 10:42 AM VMI4122.dwg

BLDG #: 930A
RACK #: RK-4959
CHASSIS ID: CFE.930.PS1
SLOT #: 10
A16 BASE ADDRESS: 0xFFFF0200
CONFIGURATION: 0 TO +10 VOLTS





TO BE DEFINED LOGICAL DEVICE # 1
TO BE DEFINES LOGICAL DEVICE # 2
TO BE DEFINED LOGICAL DEVICE # 3
TO BE DEFINED LOGICAL DEVICE # 4
TO BE DEFINED LOGICAL DEVICE # 5
TO BE DEFINED LOGICAL DEVICE # 6
TO BE DEFINED LOGICAL DEVICE # 7
TO BE DEFINED LOGICAL DEVICE # 8

JUMPER SETTINGS

ADDR BITS	0 TO +10V
E11 (A7): IN	E1: 2-3
E12 (A8): IN	E3: 1-2
E13 (A9): OUT	E4: OUT
E14 (A10): IN	E5: OUT
E15 (A11): IN	E6: 1-2
E16 (A12): IN	E7: 1-2
E17 (A13): IN	E8: 2-3
E18 (A14): IN	E9: OUT
E19 (A15): IN	E10: OUT

NOTE: PIN 1 OF JUMPERS IS DESIGNATED BY A SQUARE PAD ON THE SOLDER SIDE OF THE BOARD

REV.	ZONE	ECN NO.	DATE	BY	CKR	APP.

-3	-2	-1	REF DESIGNATOR	ITEM NO.	DESCRIPTION	MAT'L DWG NO./BML NO.
QTY REQD						

INTERPRET IN GENERAL ACCORDANCE WITH ASME Y14.24M-1989
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
DECIMAL TOLERANCES .X ±.030 .XX ±.015 .XXX ±.005
ANGULAR TOLERANCE ± .5°
NA FINISH
BREAK SHARP EDGES MAX. XX MIN. XX
OUTSTANDING ECN

Q.A. CATEGORY A-3
DRAWN BY PAPE 05/02/02
CHECKED BY
DESIGN APPROVAL
ENGINEER APPROVAL
SUPVR. APPROVAL
NEXT ASSEMBLY

BROOKHAVEN NATIONAL LABORATORY
UPTON, N.Y. 11973
C-A CONTROLS WIRING DIAGRAM
BAF, VMIVME 4122 CFE-BSAF-PS1
ANALOG OUTPUTS

SIZE A DRAWING NUMBER REV. A
MATERIAL: —— SCALE: NA WEIGHT: – SHEET 13 OF 18

*AUTOCAD